ABSTRACT OF THE DISCLOSURE

In forward direction pulse shift, by turning off a sixteenth transistor, a through-current is prevented from flowing between the fifth transistor and the seventh transistor. In backward direction pulse shift, by turning off the fifteenth transistor, a through-current is prevented from flowing between the fifth and sixth transistors. Thus, a potential variation in an output signal of a shift register between the forward direction pulse shift and the backward direction pulse shift is prevented.